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Each book has its strength, and Plummer's book stands out in terms of its broad, in-depth coverage of modern silicon CMOS technology. It doesn't cover MEMS or other exotic nanofabrication. Many students who've read the book said that Chap 2, where a modern CMOS process flow is described step by step from the substrate to back-end, was the best ...

Silicon VLSI Technology: Fundamentals, Practice, and ...

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The SiO₂ is etched and an arsenic implant forms the N⁺ regions. SILICON VLSI TECHNOLOGY Fundamentals, Practice and Modeling By Plummer, Deal and Griffin 30 © 2000 by Prentice Hall Upper Saddle River, NJ. fSolutions Manual N⁺ N⁺ P P⁺ P⁺ N N⁺ P The N⁺ regions are driven-in to their final junction depth.

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